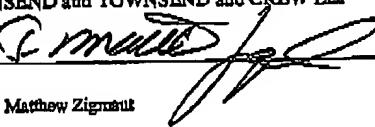


**OFFICIAL**

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, Fax No. 1-703-872-9306 on May 23, 2004.

TOWNSEND and TOWNSEND and CREW LLP

By: 

J. Matthew Zignani

PATENT

Attorney Docket No.: 020408-000300US

RECEIVED

CENTRAL FAX CENTER

MAY 24 2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Vikram Gupta

Application No.: 09/618,971

Filed: July 19, 2000

For: METHOD FOR DESIGNING  
MIXED SIGNAL INTEGRATED  
CIRCUITS CONFIGURABLE  
SYNCHRONOUS DIGITAL NOISE  
EMULATOR CIRCUIT

Customer No.: 20350

Confirmation No. 1379

Examiner: Kandasamy Thangavelu

Technology Center/Art Unit: 2123

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the office action mailed November 24, 2003, please enter the following amendments and remarks:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to the Drawings.** Formal drawings were Express Mailed May 21, 2004.

**Remarks/Arguments** begin on page 9 of this paper.